

# A Review: Spatial Parallelism to Enhance Processor Speed

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## ABSTRACT

Parallelism is the most common and very important technique for getting speedup in processors. This paper presented a concise review on some of the existing types of parallelism and the application area of that parallelism. The quantity of parallelism utilized in spatial parallelism depends most effectively at the wide variety of independent tasks. These days multiprocessor systems are highly in demands because multiprocessor systems provides higher performance, lower costs and sustained productivity in real life applications. Multiprocessor systems are used in many areas such that – petroleum exploration, medical, weather forecasting, Artificial Intelligence, expert system, sensors, military defiance etc.

**Keywords :** Parallelism, Temporal Parallelism, Spatial Parallelism, Data Parallelism, Task Parallelism, Speculative Parallelism.

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## INTRODUCTION

Today's processors use Parallelism for getting speedup. Parallelism means two or more than two operations can perform simultaneously. Although there are some conditions for parallelism[1]. Based on that parallelism conditions, every operation is can not operate in parallel manner [2]. There are various form of parallelism. In next segment some form of parallelism is described.

In order to increase the speed of a processor, various techniques are adopted like increasing the clock speed, widening data bus and multiplicity of functional units etc. Parallelism is required for getting speedup and better performance. Research and development of multiprocessor systems are aimed to improving throughput, reliability, flexibility and availability[2].

All computing system are constructed from interconnected components and depending on the level of abstraction at which a system is viewed, those components may be transistor, gates, registers, reminiscences, or entire processors. At

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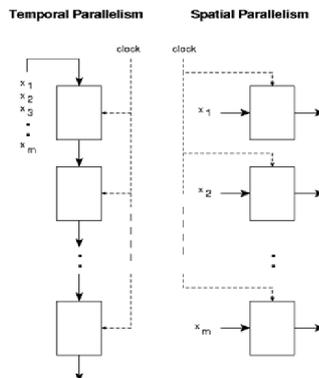
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all degrees of abstraction there are fundamental methods wherein complements can be composed to create parallel computing system, one regarding temporal parallelism and the alternative spatial parallelism.

Spatial parallelism operates in a distinct manner. Here the component used to carry out the processing task is not subdivided however is instead replicated, So that every unit of statistics (x1 - xm inside the Fig. 1) is processed with the aid of its own devoted thing. To make the most this shape of

parallelism, the units of data processed via the authentic (nonparallel) factor need to be partitionable. In other words the project area must be parallelized. An average example from the spatial parallelism of regular human hobby is the acquainted row of checkout desks observed in supermarkets.



**Figure 1:** Temporal And Spatial Parallelism

Chakravarty and others have defined the layout and implementation technique of the programming model for nested data parallelism into the Glasgow Haskell Compiler design and they have found some programs which are able to achieve good absolute performance and excellent speedups [3]. Massingill have advanced one prototype which reveals each the task and statistics parallelism. He has explained briefly task and data parallelism [4]. In keeping with Emily Mentat Programming Language (MPL) can support on control Parallelism however addition of spatial parallelism in MPL is possible. So, Emily have proposed a hard and fast of facts parallel extensions to the MPL and for that a new form of mentat magnificence, the data parallel mentat elegance is added in MPL [5].

According to Lin and et al. programs which can executes in parallel are often written in the singular program many data (SPMD) form for exploiting data parallelism in the applications. But, for parallel programs, function parallelism is also important. So, they have described a trendy methodology for exploiting feature parallelism in Single s/w more than one data packages and discussed the concerns involved in figuring out such parallelism with the multithreading facility [6].

Martti have implemented of the temporal parallelism and thread parallelism in computers [7]. W. Daniel Hillis and guy L. Steele have written an

article which describes all algorithms which are appropriate for fine grain parallel computers. They've positioned all that algorithms under statistics parallel algorithms due to the fact parallelism of that algorithms come from simultaneous operations over large data sets [8].

Consistent with Scott and et al. streaming applications process probable countless streams of fact and they accept as true with that during in streaming context replication of operators is statistics parallelism because each operator replica performs the same task on a different set of the data. They have described one compiler and runtime system that are capable of automatically extracting data parallelism from streaming applications [9].

According to N.Sivaramakrishnan and V. Subramaniaswamy huge volume of data requires of parallel computing, to ease the process of producing guidelines. The treasured hint generated with the help of either by using the use of assignment parallelism or control parallelism. They have modified the existing recommender algorithms in order to fit the requirements of the user. In addition, taking the complete advantage of heterogeneous form of computing combines both serial and parallel computation to produce high quality recommender systems [10].

Vladimir Ruchkin and others have described numerous applications for complex processing of video, images, communications, Cyber-Physical Systems (CPS), security and other signals. Mass parallelism is mainly found at the data level Parallelism (DLP). In such applications, the kinds and volume of DLP are defined by the specifications of computing hardware [11].

According to Mark C. Jeffrey, Victor A. Ying, Suvinay Subramanian, Hyun Ryong Lee, Joel Emer and Daniel Sanchez speculative parallelism, such as thread level speculation (TLS) and transactional memory (TM), have two major beneüts over nonspeculative systems: they simplify parallel programming and uncover plentiful parallelism in lots of hard-to-parallelize applications [12].

In the year of 2012, with the asist of Open Multi Processing (OMP) simulator authors Patel & Kumar have analysed the effect of problem size on parallelism. They have concluded that parallelism can be useful only beyond a certain problem size

and they have found that optimum problem size for matrix multiplication in dual and quad core processor. But, they have found the optimum problem size for only one problem i.e. matrix multiplication [13].

Later in 2017, they compared the temporal and spatial parallelism and comparison will help to select the type of parallelism for a certain type of problem. For comparison they have taken 5 processors and 5 programs which have 5 number of instructions [14-16].

## CONCLUSION

This paper provides a concise review of some existing types of parallelism and its application area. There are various forms of parallelism exists, few of them has shown in this paper. The paper starts with introduction of parallelism and summarises some existing papers in concise way. The new type of spatial will be provided which will have better system utilization and speedup. By our proposed algorithm we will try to improve the performance parameter such as throughput, delay, speedup, etc.

## REFERENCE

- [1] Bernstein A.J.: Analysis of Programs for Parallel Processing: IEEE Transactions on Electronic Computers, Vol. EC-15, No. 5, Oct, 1966, pp 757-763.
- [2] Patel, R., Kumar, S.: Visualizing Effect of dependency in SuperScalar Pipelining: published in the Proc. of 4th IEEE International Conference on Recent Advances in Information Technology RAIT, at Indian Institute of Technology (ISM), Dhanbad on 15-17 March, 2018 ,(ISBN-978-1-5386-3038-9/18), pp 716-720.
- [3] Chakravarty, M.,M.,T., e.,al. :Data Parallel Haskell: a status report, Proceedings of the 2007 workshop on Declarative aspects of multicore programming pp 10-18.
- [4] Massingill, Berna.:Integrating Task and Data Parallelism, Thesis submitted to In Partial Fullment of the Requirements for the Degree of Master of Science California Institute of Technology Pasadena California, 1993 .
- [5] Emily, A., West.:Combining Control and Data Parallelism: Data Parallel Extensions to the Mentat Programming Language, Computer Science Report No. CS-94-16 May 18, 1994.
- [6] Lin Liu, Yung., Chung-Ta King, Hau-Yang Cheng .:High Performance Computing on Networks of Workstations through the Exploitation of Function Parallelijsw, Proceedings of 3rd International Conference on Algorithms and Architectures for Parallel YungProcessing, 0-7803-4229-1 , 6-08-2002.
- [7] Martti, Forselle.:Implementation of Instruction-Level and Thread-Level Parallelism in Computers Academic Dissertation, University Of Joensu Computer Science Dissertations 2. -5386-1442-6.
- [8] Hillis, . W. Daniel , L.Steele, Guy., Jr.:Data Parallel Algorithm,Communications of the ACM, December 1986 ,Volume 29, Number 12, pp 1170-1183.
- [9] Schneider, Scott., Hirzel, Martin., Gedik, Bugra., Wu, Kun-Lung .:Safe Data Parallelism for General Streaming, IEEE TRANSACTIONS ON COMPUTERS, VOL. 64, NO. 2, FEBRUARY 2015.
- [10] Sivaramakrishnan, N., Subramaniaswamy, V.: GPU-based Collaborative Filtering Recommendation System using Task parallelism approach,Proceedings of the Second International conference on I-SMAC (IoT in Social, Mobile, Analytics and Cloud), (I-SMAC 2018) IEEE Xplore Part Number:CFP18OZV-ART; ISBN:9781.
- [11] Vladimir, Ruchkin., Marat, Mahmudov., Vitaly, Romanchuk., Vladimir, Fulin., Boris, Kostrov., Ekaterina, Ruchkina. : Smart Compiler Embedded Computing Systems Based on Cluster Parallelis, 5th Mediterranean Conference on Embedded Computing , MECO 2016 Bar, Montenegro.
- [12] Mark, C., Jeffrey, Victor, A., Ying., Subramanian, Suvinay., Lee, Hyun Ryong., Emer, Joel., Sanchez, Daniel.: Harmonizing Speculative and Non-Speculative Execution in Architectures for Ordered Parallelism,2018, 51st Annual IEEE/ACM International Symposium on Microarchitecture.
- [13] Patel, R., Kuma,r S. :Effect of Problem Size on Parallelism, Conference: 2nd International Conference on Biomedical Engineering & Assistive Technologies, At NIT Jalandhar, December 2012 .
- [14] Patel, R., Kumar, S. :Simulation and Comparison of Temporal Parallelism and Spatial Parallelism, International Journal of Computer & Mathematical Sciences, IJCMS, (ISSN 2347 – 8527) Vol. 6, Issue 9 September 2017, pp 130-136.
- [15] Raja, Rohit., Sinha, Tilendra Shishir., Dubey, Ravi Prakash .: Orientation Calculation of human Face Using Symbolic techniques and ANFIS, Published in International Journal of Engineering and Future Technology, Vol. 7, 2016, Iss.7, pp. 3750, ISSN: 2455-6432.
- [16] Khan, Rehmat., Raja, Rohit.: Introducing L1- Sparse Representation Classification for facial expression, Published in Imperial Journal of Interdisciplinary Research (IJIR), Vol. 2, 2016,Iss. 4, pp. 115-122, ISSN: 2454-1362.